**ATmega328p Fuses and Lock Bits**

BODLEVEL = 2V7

RSTDISBL = [ ]

DWEN = [ ]

SPIEN = [X]

WDTON = [ ]

EESAVE = [ ]

BOOTSZ = 256W\_3F00

BOOTRST = [X]

CKDIV8 = [ ]

CKOUT = [ ]

SUT\_CKSEL = EXTXOSC\_8MHZ\_XX\_258CK\_14CK\_65MS

EXTENDED = 0xFD (valid)

HIGH = 0xDE (valid)

LOW = 0xDE (valid)

LB = NO\_LOCK

BLB0 = NO\_LOCK

BLB1 = LPM\_SPM\_DISABLE

LOCKBIT = 0xCF (valid)